

CC014144



JAYANARLAI NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

KAKINADA - 533 003, ANDHRA PRADESH, INDIA

Mr. Karri Sairamakerishna Buchireddy

S/o Karri Buchi Reddy

having fulfilled the academic requirements and passed the examination
held during *July 2011* in ***Second Class***

has this day been admitted by the executive council to the degree of

Bachelor Of Technology

(Electronics & Communication Engineering)

Given under the Seal of the University

HTNo : 07A11A0428

Date : 24 October, 2011

CONTROLLER OF EXAMINATIONS

DIRECTOR OF EVALUATION

REGISTRAR



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

KAKINADA - 533 003, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



Roll No: K00054996

Serial No: 0000000000

Name: KARTI BATHAMAKRISHNA DUCHINEDDY

Registration No: 07A11A0428

Year of Admission: 2007 - 2008

Year of Admission: 2007 - 2008

Name of the College: NEWTON INSTITUTE OF ENGG

Year of Final Exam: B.Tech 2011

Class Awarded: Second Class

COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS
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I YEAR

1. APPLIED PHYSICS	15	20	44	4	2. C PRG. & DATA STRUCTURES	14	23	47	6
3. ELECTRONIC DEVICES & CRTS.	1	12	40	6	4. ENGINEERING DRAWING	12	29	41	4
5. ENGLISH	12	41	53	4	6. MATHEMATICAL METHODS	14	43	57	6
7. MATHEMATICS - I	11	29	40	6	8. NETWORK ANALYSIS	12	40	52	4
9. COMPUTER PROGRAMMING LAB.	22	42	64	4	10. ELECTRONIC DEV. SOI TS LAB	22	41	63	4
11. ENGLISH LANG. COMM. SKILLS LAB	13	10	59	4	12. IT WORKSHOP	21	41	62	4

II YEAR

1. ELECTRICAL TECHNOLOGY	12	20	41	4	1. ANALOG COMMUNICATIONS	15	20	41	4
2. ELECTRONICS CIRCUIT ANALYSIS	14	23	42	4	2. CONTROL SYSTEMS	15	25	40	4
3. ENVIRONMENTAL STUDIES	15	32	43	4	3. ELECTRO MAGNETIC WAVES & TRANSMISSION	14	25	43	4
4. MATHEMATICS - III	10	30	40	4	4. OBJECT ORIENTED PROGRAMMING	14	26	42	4
5. PROBABILITY THEORY & STO. PROCESSES	14	31	45	4	5. PULSE & DIGITAL CIRCUITS	13	26	43	4
6. SIGNALS & SYSTEMS	11	30	41	4	6. SWITCHING THEORY & LOGIC DESIGN	12	22	44	4
7. ELECTRICAL TECHNOLOGY (LAB)	22	46	68	2	7. ANALOG COMMUNICATIONS (LAB)	22	43	64	2
8. ELECTRONIC CIRCUITS (LAB)	21	40	64	2	8. PULSE & DIGITAL CIRCUITS (LAB)	22	42	64	2

III YEAR

1. ANTENNAS AND WAVE PROPAGATION	14	13	57	4	1. DIGITAL SIGNAL PROCESSING	13	20	42	4
2. COMPUTER ORGANIZATION	15	32	47	4	2. MANAGEMENT SCIENCE	16	21	43	4
3. DIGITAL COMMUNICATIONS	13	20	41	4	3. MICRO PROCESSORS AND INTERFACING	15	20	43	4
4. DIGITAL IC APPLICATIONS	15	28	43	4	4. MICROWAVE ENGINEERING	16	21	43	4
5. LINEAR IC APPLICATIONS	12	46	50	4	5. TELECOMMUNICATION SWITCHING SYSTEMS	15	23	44	4
6. MANAGERIAL ECONOMICS AND FINANCIAL	12	10	52	4	6. VLSI DESIGN	16	24	54	4
7. IC APPLICATIONS AND CAD LAB.	23	44	67	2	7. ADVANCED ENGLISH COMMUNICATIONS	15	45	62	2
8. DIGITAL COMMUNICATIONS LAB	22	45	67	2	8. MICRO PROCESSORS AND INTERFACING	23	42	64	2

IV YEAR

1. CELLULAR AND MOBILE COMMUNICATIONS	15	32	47	4	1. COMPREHENSIVE VIVA	0	22	22	2
2. COMPUTER NETWORKS	16	37	53	4	2. DSP PROCESSORS AND ARCHITECTURES	15	21	51	4
3. DIGITAL IMAGE PROCESSING	16	29	45	4	3. EMBEDDED AND REAL TIME SYSTEMS	15	29	44	4
4. ELECTRONIC MEASUREMENTS & INSTRUMENTS	13	31	44	4	4. OPTICAL COMMUNICATIONS	19	1	20	2
5. RADAR SYSTEMS	10	23	46	4	5. SEMINAR	42	1	43	2
6. TELEVISION ENGINEERING	15	24	51	4	6. INDUSTRY ORIENTED MINI PROJECT	0	16	16	2
7. DIGITAL SIGNAL PROCESSING LAB	23	44	67	2	7. PROJECT WORK	27	157	184	10
8. MICROWAVE AND OPTICAL COMMUNICATIONS	22	45	67	2					

Number of Credits (Required): 224

Approved Pass Marks: 216 Credits 3340 out of 5350 (58.69%)

Date of Declaration (Final): AUGUST 2011

(Signature of Controller of Examinations)

(Signature of Head of Institution)

02/08/2011

CONTROLLER OF EXAMINATIONS